

## EAST Search History

10/6/14, 215

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	299	(714/49).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:32
L2	1007	(714/48).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:32
L3	144	(714/51).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:32
L4	266	(714/12).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:33
L5	264	(714/744).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:33
L6	938	replay\$3 adj (system or instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:36
L7	245752	(detect\$3 or track\$3 or monitor\$3) adj3 (fault\$3 or error\$3 or fail\$3 or problem or malfunction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:39
L8	882	synchroniz\$4 adj (fault\$3 or error\$3 or fail\$3 or problem or malfunction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:40

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L9	869	match\$3 adj instruction	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:40
L10	3	7 same 8 same 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:40
L11	3	6 same 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:41
L12	3	6 and 7 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:41
L14	357	asynchronous adj5 pipeline	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:44
L15	38	asynchronous adj fault\$	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:45
L16	37454	sequence adj number	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:47
L17	39	comparator same buffer same 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:57

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L18	2	17 same 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:57
L19	2	17 same 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:58
L20	2	17 and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:58
L21	2	6 and 7 and (age adj guard\$3 adj logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:59
L22	550	(712/218).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:59
L23	319	(712/219).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 22:59
L24	310	(lee-yung\$ or carmean-douglas\$ or vidwans-rohit\$).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/15 23:03

10/6/14 215


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Best 200 shown

**81** [Physical Experimentation with Prefetching Helper Threads on Intel's Hyper-Threaded Processors](#)  
 Dongkeun Kim, Steve Shih-wei Liao, Perry H. Wang, Juan del Cuillo, Xinmin Tian, Xiang Zou, Hong Wang, Donald P. Shen  
 March 2004 **Proceedings of the international symposium on Code generation and optimization: feedback runtime optimization CGO '04**

**Publisher:** IEEE Computer SocietyFull text available: [pdf\(264.47 KB\)](#)Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

Pre-execution techniques have received much attention as an effective way of prefetching cache blocks to tolerate increasing memory latency. A number of pre-execution techniques based on hardware, compiler, or both have been extensively by researchers. They report promising results on simulators that model a Simultaneous Multithreading paper, we apply the helper threading idea on a real multithreaded machine, i.e., Intel Pentium 4 processor with H

**82** [The nesC language: A holistic approach to networked embedded systems](#)

David Gay, Philip Levis, Robert von Behren, Matt Welsh, Eric Brewer, David Culler

 May 2003 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2003 conference on Programming language implementation PLDI '03**, Volume 38 Issue 5
**Publisher:** ACM PressFull text available: [pdf\(177.98 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present nesC, a programming language for networked embedded systems that represent a new design space for developers. An example of a networked embedded system is a sensor network, which consists of (potentially) thousands of "motes," each of which execute concurrent, reactive programs that must operate with severe memory and power constraints. The contribution is to support the special needs of this domain by exposing a programming model that incorporates .

**Keywords:** C, TinyOS, components, concurrency, data races, first-order, modules, nesC, programming language

**83** [Evaluation of a concurrent error detection method for microprogrammed control units](#)

A. Bailas, L. L. Kinney

January 1988 **Proceedings of the 21st annual workshop on Microprogramming and microarchitecture****Publisher:** IEEE Computer Society PressFull text available: [pdf\(1.33 MB\)](#)Additional Information: [full citation](#), [references](#), [index terms](#)

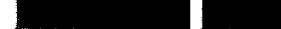
**84** [Debugging concurrent programs](#)

10/6/4,215


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